# **Reconfigurable Fast Fourier Transform on PYNQ**

This project demonstrates the implementation of FFT-based signal reconstruction on a PYNQ board, leveraging the programmable logic for efficient computation. It involves creating a random input signal and performing a Fast Fourier Transform (FFT) to analyze its frequency components. It also performs FFT on both PS and PL parts of the PYNQ board and subsequently computes the hardware acceleration factor.